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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Tadashi MATSUDA

SERIAL NUMBER: 10/821,956

GROUP: 2826

FILED: April 12, 2004

EXAMINER: FETSUM, Abraham

FOR: INSULATED GATE BIPOLAR TRANSISTOR HAVING TRENCH GATES OF
RECTANGULAR UPPER SURFACES WITH DIFFERENT WIDTHS

REQUEST TO CORRECT TITLE OF INVENTION


MAIL STOP ISSUE FEE
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SIR:

In the matter of the above-identified application for patent, we hereby request
correction of your records to reflect the correct title of the invention. The title of the
invention should read as follows: INSULATED GATE BIPOLAR TRANSISTOR HAVING
TRENCH GATES OF RECTANGULAR UPPER SURFACES WITH DIFFERENT
WIDTHS.

Respectfully Submitted,

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